L Number	Hits	Search Text	DB	Time stamp
46	1	("pipelined bus" and soc) and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03 08:08
47	21	((pipeline\$1 near3 bus) and soc) and @ad<20010626	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/06/03 08:09
45	282	("pipelined bus") and @ad<20010626	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/06/03 08:17
48	1	("pipelined bus" with (flip-flop flipflop "flip flop")) and @ad<20010626	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03
49	. 1	("pipelined bus" same (flip-flop flipflop "flip flop")) and @ad<20010626	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03 08:36
54	14	("pipelined bus" same stage) and @ad<20010626	IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/06/03 08:29
55	32	("pipelined bus" and (flip-flop flipflop "flip flop")) and @ad<20010626	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/06/03 11:26
60	. 4	("pipelined bus" same latch\$2) and @ad<20010626	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO;	2004/06/03 08:47
63	47	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same dma same memory) and @ad<20010626	DERWENT; IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03 11:26
66	1		IBM_TDB USPAT	2004/06/03 09:18
67	6	"6601126"	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03
76	1	•	IBM_TDB USPAT	2004/06/03 09:25
77	15	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same "memory bus") and @ad<20010626	USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03
80	97	(("system on chip" "system on a chip" system-on-chip system-on-a-chip soc) same dma) and @ad<20010626	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT;	2004/06/03 11:12
87	1620	("Power PC") and @ad<20010626	IBM_TDB USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM TDB	2004/06/03 11:12

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88	3	("Power PC 405") and @ad<20010626	USPAT; US-PGPUB; EPO; JPO;	2004/06/03
			DERWENT;	
91	21480	((flip-flop flipflop "flip flop") same edge) and @ad<20010626	IBM_TDB USPAT; US-PGPUB;	2004/06/03 11:26
			EPO; JPO; DERWENT;	
92	16379	((flip-flop flipflop "flip flop") with edge) and @ad<20010626	IBM_TDB USPAT; US-PGPUB;	2004/06/03 11:26
			EPO; JPO; DERWENT;	
93	1030	((flip-flop flipflop "flip flop") with (edge\$1trigger\$2)) and @ad<20010626	IBM_TDB USPAT; US-PGPUB;	2004/06/03 11:27
			EPO; JPO; DERWENT;	
94	309	(((flip-flop flipflop "flip flop") with	IBM_TDB USPAT;	2004/06/03
		(edge\$1trigger\$2)) same latch) and @ad<20010626	US-PGPUB; EPO; JPO;	11:27
			DERWENT; IBM TDB	
95	214	(((flip-flop flipflop "flip flop") with	USPAT;	2004/06/03
		(edge\$1trigger\$2)) with latch) and @ad<20010626	US-PGPUB; EPO; JPO;	11:28
			DERWENT; IBM TDB	
96	8	(((flip-flop flipflop "flip flop") with	USPĀT;	2004/06/03
		(edge\$1trigger\$2)) with latch with known) and @ad<20010626	US-PGPUB; EPO; JPO;	11:31
			DERWENT;	
97	179	(((flip-flop flipflop "flip flop") with	IBM_TDB USPAT;	2004/06/03
		instead) with latch) and @ad<20010626	US-PGPUB; EPO; JPO;	11:33
			DERWENT;	(1)
98	1738	(((flip-flop flipflop "flip flop") with	IBM_TDB USPAT;	2004/06/03
		(benefit advantage))) and @ad<20010626	US-PGPUB;	11:33
	0		EPO; JPO; DERWENT;	
99	1683	(((flip-flop flipflop "flip flop") with	IBM_TDB USPAT;	2004/06/03
99	1003	(((TIIP-IIOP IIIPIIOP IIIP IIOP , with (advantage))) and @ad<20010626	US-PGPUB;	11:33
			EPO; JPO; DERWENT;	
100		///Slim Slam SlimSlaw WSlim SlamWy with	IBM_TDB	2004/06/02
100	95	(((flip-flop flipflop "flip flop") with (advantage) with latch)) and @ad<20010626	USPAT; US-PGPUB;	2004/06/03 13:35
			EPO; JPO; DERWENT;	
			IBM_TDB	
101	1	<pre>((registered with interconnect) same (point\$1to\$1point)) and @ad<20010626</pre>	USPAT; US-PGPUB;	2004/06/03 13:36
			EPO; JPO;	
			DERWENT; IBM_TDB	
102	0	(("registered with interconnect")) and @ad<20010626	USPAT; US-PGPUB;	2004/06/03 13:36
			EPO; JPO;	23.30
		·	DERWENT; IBM_TDB	
103	6	(("registered interconnect")) and @ad<20010626	USPAT; US-PGPUB;	2004/06/03
		Gad/20010020	EPO; JPO;	13:37
	λ.		DERWENT; IBM TDB	
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104	137	("bus interface" with (flip\$1flop	USPAT;	2004/06/03
		flipflop)) and @ad<20010626	US-PGPUB;	13:53
			EPO; JPO; DERWENT;	
			IBM TDB	
105	22	((flip\$1flop flipflop) adj interface) and	USPAT;	2004/06/03
103	22	@ad<20010626	US-PGPUB;	14:04
		Caarboroop	EPO; JPO;	1
	1		DERWENT;	
			IBM TDB	
106	238	(point\$1to\$1point adj bus) and	USPAT;	2004/06/03
		@ad<20010626	US-PGPUB;	14:18
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0004/05/00
107	1143	(handshake with bus) and @ad<20010626	USPAT;	2004/06/03
			US-PGPUB;	14:18
			EPO; JPO; DERWENT;	
			IBM TDB	
108	5	(handshake with bus with advantage) and	USPAT;	2004/06/03
100		@ad<20010626	US-PGPUB;	14:19
		644.20020020	EPO; JPO;	
			DERWENT;	
			IBM_TDB	·
109	293	(handshake.ti.) and @ad<20010626	USPAT;	2004/06/03
			US-PGPUB;	14:20
			EPO; JPO;	
			DERWENT;	
1.00		1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1	IBM_TDB	2004/06/02
110	58	(handshake.ti. and bus) and @ad<20010626	USPAT;	2004/06/03 15:30
			US-PGPUB; EPO; JPO;	15:30
			DERWENT;	
			IBM TDB	
116	238	(712/233.ccls.) and @ad<20010626	USPAT;	2004/06/03
		(122, 200, 200, 7, 200,	US-PGPUB;	15:33
	·		EPO; JPO;	
			DERWENT;	·
			IBM_TDB	
111	896	(710/305.ccls.) and @ad<20010626	USPAT;	2004/06/03
			US-PGPUB;	15:36
			EPO; JPO; DERWENT;	
			IBM TDB	
112	187	(710/106.ccls.) and @ad<20010626	USPAT;	2004/06/03
112	10/	(/10/100.0015.) and Gad\20010020	US-PGPUB;	15:40
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	
113	963	(710/100.ccls.) and @ad<20010626	USPAT;	2004/06/03
			US-PGPUB;	15:43
			EPO; JPO;	
			DERWENT;	
1114	53	(710/118.ccls.) and @ad<20010626	IBM_TDB USPAT;	2004/06/03
114	53	(/IU/IIO.CCIS.) and ead<20010020	US-PGPUB;	15:46
			EPO; JPO;	10.10
			DERWENT;	
			IBM TDB	
115	31	(710/125.ccls.) and @ad<20010626	USPAT;	2004/06/03
			US-PGPUB;	15:53
	1		EPO; JPO;	
			DERWENT;	
			IBM_TDB	2004/05/22
-	141107	(("system on chip" "system on a chip"	USPAT;	2004/06/02
		system-on-chip system-on-a-chip soc)) and @ad<20010626	US-PGPUB; EPO; JPO;	15:46
		@du\20010020	DERWENT;	
	1		IBM TDB	
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-	33	(("system on chip" "system on a chip"	USPAT;	2004/06/03
		system-on-chip system-on-a-chip soc) same	US-PGPUB;	08:52
		core same bus same peripheral) and	EPO; JPO;	
	ĺ	@ad<20010626	DERWENT;	
			IBM TDB	
_	7	(("system on chip" "system on a chip"	USPAT;	2004/06/02
		system-on-chip system-on-a-chip soc) same	US-PGPUB;	16:00
		core same bus same peripheral) and	EPO; JPO;	
		pipeline and @ad<20010626	DERWENT;	
1			IBM TDB	
_	49	(("system on chip" "system on a chip"	USPAT;	2004/06/03
		system-on-chip system-on-a-chip soc) and	US-PGPUB;	08:04
1		(bus with pipeline)) and @ad<20010626	EPO; JPO;	
			DERWENT;	
			IBM TDB	